

DEC 13 2005

843.43311X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: D. HISAMOTO, et al.
Application No.: 10/726,507
Filed: December 4, 2003
For: FABRICATION METHOD AND STRUCTURE OF
SEMICONDUCTOR NON-VOLATILE MEMORY DEVICE
Art Unit: 2815
Examiner: Jesse A. Fenty

AMENDMENT

Mail Stop: Amendment
Commissioner for Patents
P.O. BOX 1450
Alexandria, VA 22313-1450

December 13, 2005

Sir:

In response to the outstanding Office Action dated, July 13, 2005, the period of response for each which extension of time is requested in the attached Petition for Extension of Time, please amend the above-identified application as listed below and as set forth on the following pages:

Amendments to the Claims; and

Remarks are included following the amendments